

Electrical activation in silicon-on-insulator after low energy boron implantation

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We have investigated the electrical activation of implanted boron in silicon-on-insulator (SOI) material using Hall effect, four-point probe, and secondary ion mass spectrometry. Boron was implanted at energies ranging from 1 keV to 6.5 keV with a dose of $3 \times 10^{14} \text{ cm}^{-2}$ into bonded SOI wafers with surface silicon thickness ranging from 300 Å to 1600 Å. In one sample set, furnace anneals at 750 °C were performed in a nitrogen ambient for times ranging from 5 min to 48 h. A second sample consisted of isochronal furnace anneals performed from 450 °C to 1050 °C for 30 min. Significantly less activation of boron is observed in SOI at temperatures below 750 °C, regardless of the implant energy and surface silicon thickness. Between 750 °C and 900 °C, the active dose of boron in SOI is similar to that of bulk Si. As the implant energy increases, the fractional activation in thin SOI increases, due to a reduction in boron interstitial clusters (BIC) in the surface Si layer. It is concluded that an increase in the BIC population is the likely source of the low activation observed in SOI. This may be due to an increase in the interstitial supersaturation within the surface Si layer, due to the interface acting as a reflective boundary for interstitials. © 2004 American Institute of Physics. [DOI: 10.1063/1.1769095]

I. INTRODUCTION

Silicon-on-insulator (SOI) appears slated to be the replacement for bulk silicon as complementary metal-oxide-semiconductor (CMOS) devices approach the 50–60 nm technology node.¹ This is a result of the performance and fabrication advantages SOI provides.^{2–5} These include increased speed, reduced power consumption, reduced short channel effects, and elimination of latchup, just to name a few. Double-gate SOI structures such as the FinFET,⁶ PAGODA,⁷ gate-all-around,⁸ and DELTA,⁹ each show promise over the traditional planar CMOS process. However, models for predicting dopant diffusion and activation in SOI are fairly limited at present.¹⁰ Physically based models will be essential for design and fabrication of future SOI devices.

One of the challenges facing SOI model development is the presence of two interfaces. These include the traditional native oxide/silicon and surface silicon/buried oxide (BOX) interface. This results in anomalous doping profiles for both *n*- and *p*-type dopants.^{11–17} As the surface silicon layer is thinned, boron tends to pileup at the surface Si/BOX interface, while phosphorus and arsenic tend to deplete.¹¹ For thick surface Si films and shallow implants, dopant diffusion appears similar to bulk Si.^{11,18} However, the effect of dopant

segregation in SOI on electrical activation has yet to be thoroughly investigated and will be critical to scaling SOI devices into the fully depleted regime.

Formation of boron interstitial clusters (BICs) is a major problem in attempting to activate junctions. Instead of the maximum active carrier concentration being limited by solid solubility of boron, BICs form at much lower concentrations. It could be hypothesized that the segregation phenomenon might affect the degree to which certain dopants tend to cluster as they pileup or deplete at the interface. Simulations by Vuong *et al.*,¹² suggest that clustering of boron in SOI is similar to bulk Si.

Robinson *et al.* investigated electrical activation of arsenic in separation by implantation of oxygen substrates and found little difference to bulk Si.¹⁶ Other investigations of mobility in SOI metal-oxide-semiconductor field effect transistors (MOSFETs) have found reduced electron and hole mobility due to phonon scattering as the surface Si thickness is reduced.^{19–22} On the other hand, for thicker SOI films the mobility in SOI is enhanced due to volume inversion.^{23,24} However, these cases for MOSFETs are quite different from that of SOI material that has not been fabricated into a device. This experiment set out to investigate the effect of surface Si thickness and dopant segregation on electrical activation of implanted boron.

II. EXPERIMENT

A. Material processing

In the experiment 200 mm, (001), *p*-type, 14–22 Ω cm UNIBOND® and Czochralski substrates were used. All the SOI substrates had a BOX thickness of 400 nm. The SOI substrates, having an initial surface Si thickness of 1600 Å, were thinned to 700 Å and 300 Å using oxidation and etching in dilute HF (10:1). Prior to ion implantation, a screen oxide was thermally grown in a wet oxygen ambient to help reduce channeling of the boron ions. Room temperature non-amorphizing ion implantation of $^{11}\text{B}^+$ at a dose of $3 \times 10^{14} \text{ cm}^{-2}$ was performed for energies ranging from 1 keV to 6.5 keV at a 7° tilt and 22° twist angle. Implant energies were designed to place the projected range of the implant at varying depths within the surface Si layer. Following the implants, a low temperature oxide (LTO) of 20 nm was deposited at 425 °C in order to prevent dopant out diffusion of the shallow implants. This was performed using a plasma enhanced chemical vapor deposition system with the screen oxide still in place. Specimens were scribed into squares ranging from 10 mm \times 10 mm to 14 mm \times 14 mm. Anneals were performed in a Lindberg quartz tube furnace in a nitrogen ambient. Isothermal anneals at 750 °C for times of 5 min to 48 h were done to activate the implanted boron. Separate isochronal anneals for 30 min at temperatures of 450–1050 °C was also performed to understand the temperature dependence of the activation process.

B. Analytical techniques

Hall effect was performed using an MMR Technologies system with a MPS-50 programmable power supply and H-50 Hall, van der Pauw field controller. A magnetic field of 3000 G was used in all of the measurements. Current was varied from 1×10^{-7} A to 1×10^{-3} A in order to check the linearity of the Hall measurements. This allowed measurement of the hole mobility, sheet number, and sheet resistance. Hall scattering factors were determined by annealing specimens at 1000 °C for 2 h to obtain complete activation of the specimens. The active dose obtained was then divided by the implanted dose to yield the scattering factor. These were approximately 0.9, 0.9, 0.7, and 0.85, for the 300 Å, 700 Å, 1600 Å, and bulk, respectively.

Four-point probe measurements were done using a Jandel multi height probe. A current of 438.02 μA was used for bulk Si and 38.02 μA for SOI. Geometrical correction factors were used since sample sizes greatly exceeded the probe spacing. Four-point probe was mainly used to compare sheet resistance values with those obtained from the Hall–van der Pauw, ensuring the reliability of the measurements.

Secondary ion mass spectrometry (SIMS) was performed using a CAMECA IMS-3F secondary ion mass spectrometer. An O_2^+ primary ion beam with an energy of 15 keV and 15° incident angle was used for obtaining boron concentration profiles. An ion beam current of 100 nA was used, along with an electron gun to assist with charge neutralization in the BOX. This helped to determine the amount of boron segregation towards the BOX as annealing proceeded.

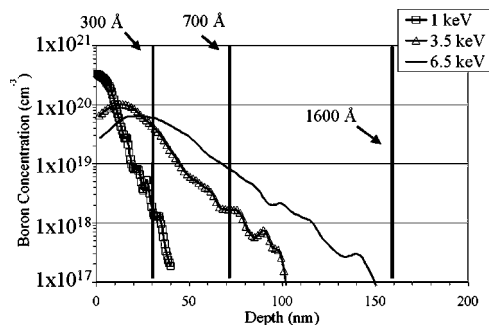


FIG. 1. UT-MARLOWE ion profile simulations for B^+ implants at 1 keV, 3.5 keV, 6.5 keV, $3 \times 10^{14} \text{ cm}^{-2}$. Note location of surface Si/BOX interface for 300 Å, 700 Å, and 1600 Å SOI.

III. RESULTS

UT-MARLOWE²⁵ simulations were used to determine the amount of as-implanted dose loss to the BOX. This was done, in the case of SOI, by truncating the ion/boron concentration profile at the surface Si/BOX interface and integrating the portion of the profile remaining in the surface Si layer. Figure 1 shows the ion concentration profiles obtained using UT-MARLOWE, while Fig. 2 shows percent dose retained calculated from the simulations. This is important to keep track of, since boron can certainly not serve as an acceptor if it lies in the BOX. Thus, it would affect the electrical measurements obtained from Hall and four-point probe. All the implant energies resulted in dose loss in the 300 Å SOI, ranging from less than 1% at 1 keV to 50% at 6.5 keV. The 700 Å SOI lost 6% of the dose at 6.5 keV, but did not lose any for the 1 keV or 3.5 keV. No dose loss to the BOX occurred in the 1600 Å SOI for any of the implants simulated.

Electrical data obtained from Hall effect measurements for the 1 keV and 6.5 keV implant energies annealed at 750 °C are shown in Figs. 3 and 4, respectively. Significantly less activation [Figs. 3(a) and 4(a)] can be seen to occur in all of the SOI specimens compared to bulk Si. Even after annealing for 48 h, the active dose in SOI does not approach that of bulk Si. The hole mobility (μ_h) and sheet resistance (R_s) are also lower in SOI by roughly 300 $\text{cm}^2/\text{V s}$. The sheet resistance in the 1600 Å SOI appears to be slightly less than the 700 Å and 300 Å. The bulk

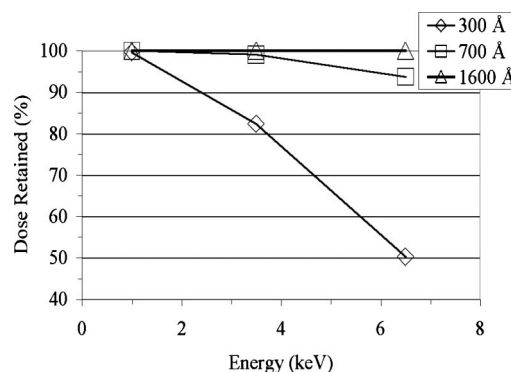


FIG. 2. Percent retained dose of boron in surface Si layer as function of implant energy for 300 Å, 700 Å, and 1600 Å SOI. Calculated using UT-MARLOWE ion profiles.

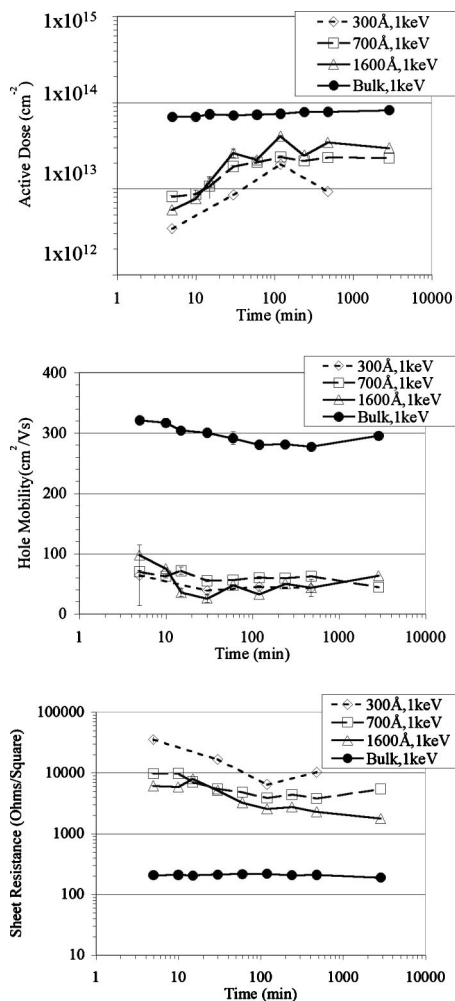


FIG. 3. Isothermal Hall data for B⁺, 1 keV, 3×10^{14} cm⁻² at 750 °C including (a) active dose, (b) hole mobility, and (c) sheet resistance.

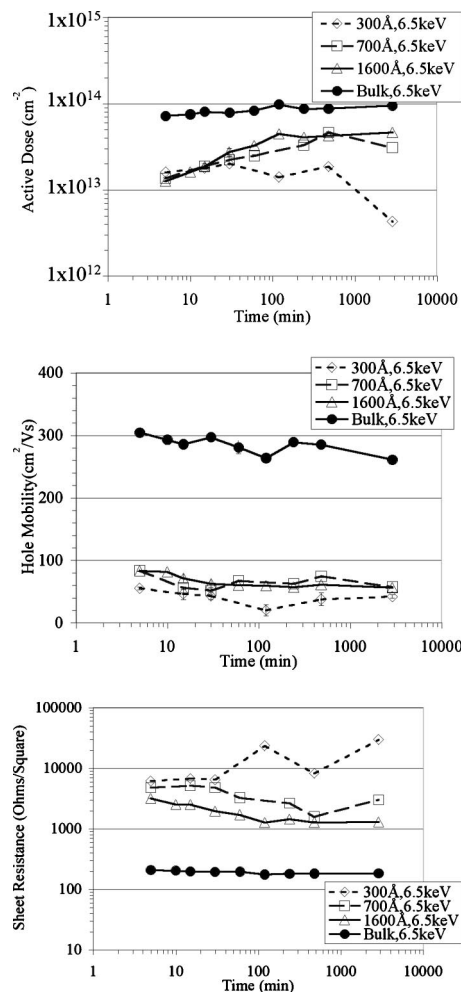


FIG. 4. Isothermal Hall data for B⁺, 6.5 keV, 3×10^{14} cm⁻² at 750 °C including (a) active dose, (b) hole mobility, and (c) sheet resistance.

Si results compare favorably with those of Ref. 26, in terms of active dose, for boron implants at similar energies and doses. However, when compared to mobilities obtained by Ref. 27 the problem appears to be that the bulk Si has an enhanced mobility rather than SOI being degraded. Sasaki *et al.*, found mobilities for boron concentrations between 1×10^{19} cm⁻³ and 1×10^{20} cm⁻³ to be between 70 and 53 cm²/V s, respectively.²⁷ Figure 5 compares the sheet resistance obtained from four-point probe with Hall effect for each of the implant energies. The R_s values agree well, indicating the measurements are indeed reliable and not a result of instrument error.

Figures 6 and 7 show the isochronal Hall effect data for the 1 keV and 6.5 keV implant energies, respectively. For the 1 keV SOI specimens, the active dose was over two orders of magnitude less compared to bulk Si for temperatures less than 600 °C. Significant activation occurs between 600 °C and 750 C in SOI. The 700 Å and 1600 Å activate slightly more than bulk si as the annealing temperature exceeded 900 °C. Once again, lower mobility and sheet resistance occur in SOI, but the mobility is close to that found in Ref. 27. A significant decrease in mobility occurs in SOI and bulk Si as the temperature increases. This is attributed to ionized impurity scattering as more boron atoms begin to

occupy Si lattice sites.²⁸ The sheet resistance in SOI does begin to approach bulk Si as the annealing temperature increases. For the 300 Å SOI implanted at 6.5 keV, the active dose is higher below 600 °C than the 700 Å and 1600 Å. This is surprising considering the increase in dose loss in the 300 Å SOI; intuitively, one would expect less dose to result in a lower active dose since carriers cannot activate in an oxide. However, it is likely due to a reduction in the BIC population for the 300 Å SOI. This is discussed further in the Discussion.

SIMS profiles for the 300 Å SOI annealed at 750 °C are shown in Fig. 8. Segregation of boron into the BOX occurs after annealing for 30 min, indicated by the depletion of boron as the surface Si/BOX interface is approached. Most segregation appears to take place in the first 30 min at 750 °C, as evidenced by the 120 min profile.

IV. DISCUSSION

The data acquired show that a significant difference exists in the activation process between SOI and bulk. Activation in bulk Si shows a diminished temperature dependence compared to SOI. However, it is difficult to directly compare the two because of the higher activation in bulk Si at low thermal budgets. This indicates that significant activation in

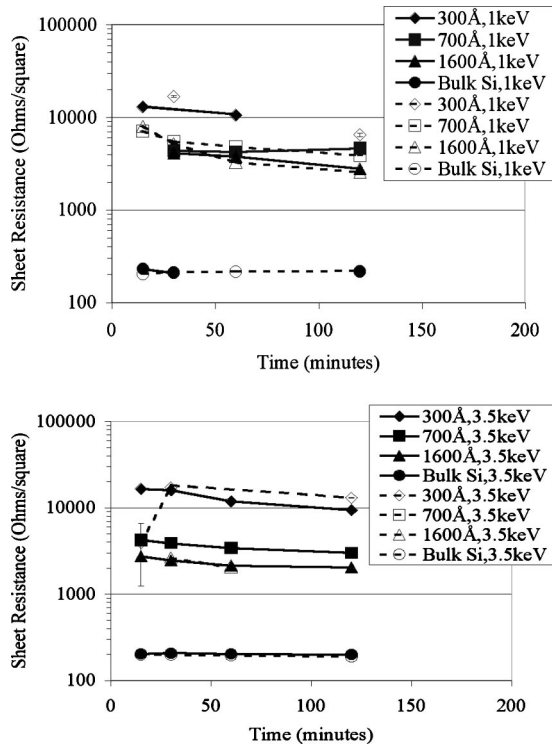


FIG. 5. Comparison of sheet resistance data measured by four-point probe and Hall effect for (a) 1 keV, (b) 3.5 keV, and (c) 6.5 keV, $3 \times 10^{14} \text{ cm}^{-2}$ annealed at 750 °C. Solid symbols and lines represent four-point probe measurements and open symbols represent Hall measurements.

bulk Si may have occurred during the LTO deposition. The boron implants were nonamorphizing, so solid phase epitaxy did not occur in the experiments. This may have explained the higher activation in bulk Si at low temperatures, but this was not the case. Significant transient enhanced diffusion (TED) was observed in the bulk Si SIMS profiles. This eliminates the possibility of other thermal processing accidentally taking place that could have activated the boron in bulk Si. This may have been a valid point if no TED was observed, but this was also not the case. It could also be speculated that the results are simply an artifact of performing Hall effect on thin Si films. If this was the case full activation would not have been obtained, yet the results in Figs. 6 and 7 show that 100% activation was obtained in thick SOI annealed at high temperatures. The effects of boron clustering, segregation, impurity trapping, and thermal strain on the electrical activation results presented above are each discussed separately.

A. Boron interstitial clusters

As indicated previously, high concentrations of boron in the presence of an interstitial supersaturation can result in the formation of boron-interstitial clusters (BICs).²⁹ This is generally accepted to occur between $1 \times 10^{18} \text{ cm}^{-3}$ and $1 \times 10^{19} \text{ cm}^{-3}$ boron concentrations.²⁹⁻³² Clustering is often observed as immobile peaks in SIMS profiles, low Hall doses, as well as reductions in the trapped interstitial population in extended defects.³³ The boron concentrations in the present study are well above the clustering limit according to Fig. 8. Thus, it could be proposed that the lack of activation

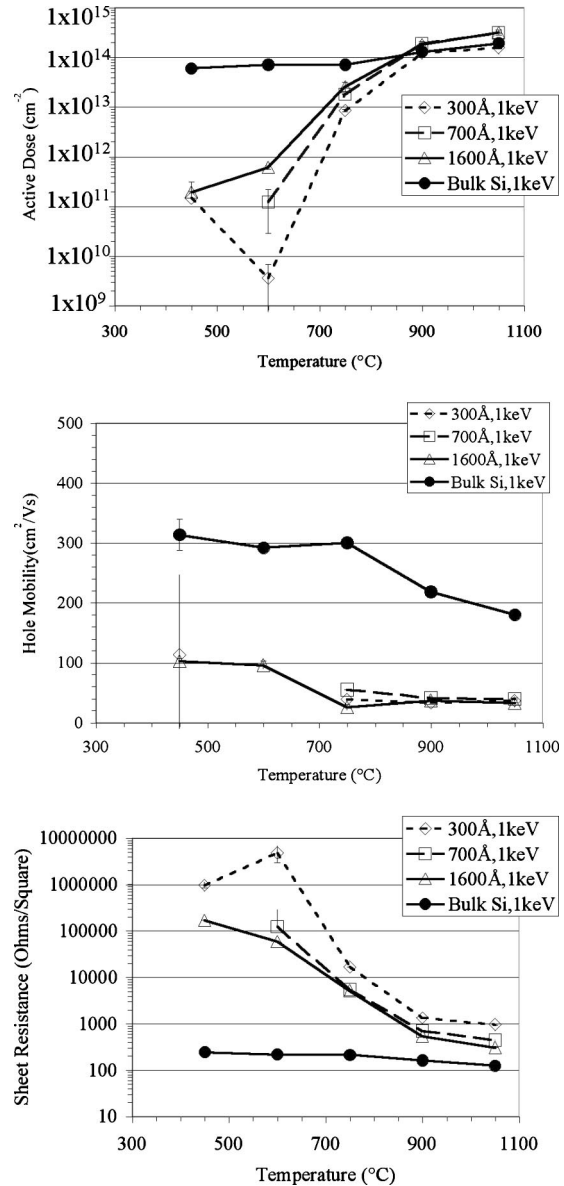


FIG. 6. Isochronal Hall data for B^+ , 1 keV, $3 \times 10^{14} \text{ cm}^{-2}$ after annealing for 30 min showing (a) active dose, (b) hole mobility, and (c) sheet resistance.

is a result of an increase in BICs in SOI. If more of the boron binds with the excess interstitials, it could reduce the electrical activation, assuming that particular BIC complex is not electrically active. Also, if a higher interstitial supersaturation is present in the surface Si layer it could provide the extra interstitials to allow for the increase in BIC population.³⁴

It has been shown that a reduction in the trapped interstitial dose in {311} defects occurs in SOI after boron implantation at 6.5 keV and 19 keV, $3 \times 10^{14} \text{ cm}^{-2}$.³⁵ The authors attributed this to an increase in BIC formation in SOI. However, the microstructure consists mainly of small dot defect clusters at 1 keV and 3.5 keV, rather than larger extended defects such as {311}s and dislocation loops. The dot defects are more difficult to accurately quantify due to their small size, thus there could be significant error in the QTEM measurements at the lowest energies. This would tend to support the BIC theory in thick SOI.

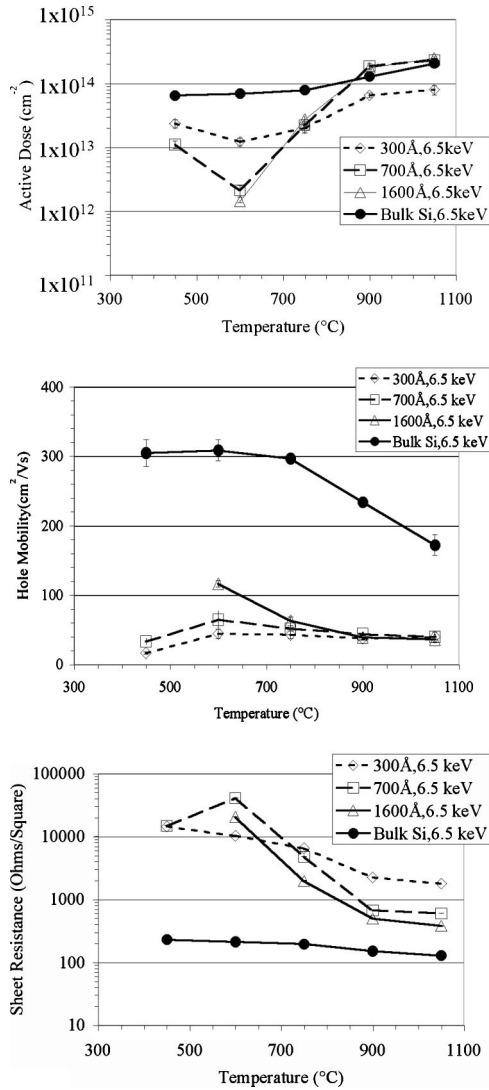


FIG. 7. Isochronal Hall data for B⁺, 6.5 keV, 3 × 10¹⁴ cm⁻² after annealing for 30 min showing (a) active dose, (b) hole mobility, and (c) sheet resistance.

A theory could be proposed suggesting that perhaps the surface Si/BOX interface actually prevents interstitials from diffusing into or recombining at the BOX. Rather, the interface tends to behave as a more reflective boundary for interstitials. Interstitials released from the extended defects would tend to remain within the surface Si layer and be available to

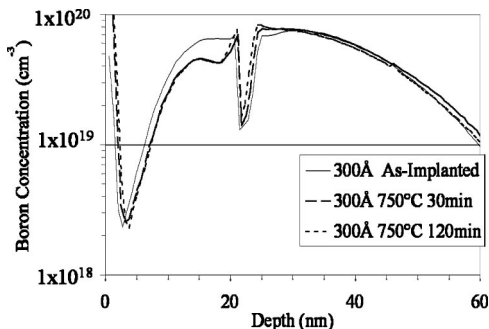


FIG. 8. Boron concentration profiles from SIMS for 300 Å SOI implanted at 6.5 keV, 3 × 10¹⁴ cm⁻² then annealed at 750 °C. Note segregation of boron into buried oxide.

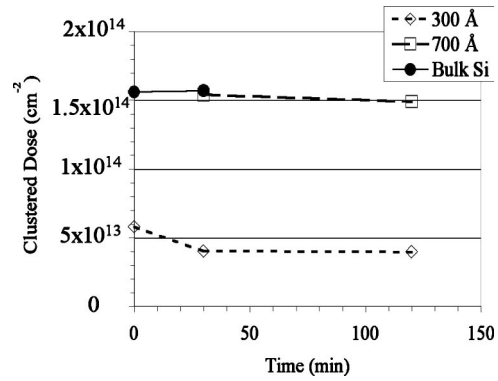


FIG. 9. Clustered dose in SOI and bulk Si for B⁺, 6.5 keV, 3 × 10¹⁴ cm⁻² annealed at 750 °C. Dose was obtained by integrating the B concentration profiles that lie above a level of 1 × 10¹⁹ cm⁻³.

participate in the BIC process. This would explain the low activation observed for SOI. This theory would not be out of the realm of possibility based on results from previous experiments that show the interface is a weak sink for interstitials unless a large amount of the dose is lost to the BOX during the implant.³⁵⁻³⁸ It has also been conjectured that the contact potential at the Si/SiO₂ interface sets up an electric field that is likely to repel interstitials for p-type material.³⁹

A BIC theory also explains why the 300 Å SOI activated more than the thicker SOI at low temperatures for the 6.5 keV implant. Figure 9 shows the clustered dose in SOI and bulk Si for the 6.5 keV annealed at 750 °C. The clustered dose was obtained by subtracting 1 × 10¹⁹ cm⁻³ from the boron concentration in the SIMS profiles and then integrating over the surface Si thickness. The significant reduction in clustered dose for the 300 Å SOI explains the higher activation that was measured despite the dose loss of boron to the BOX. This is due to an immediate loss of interstitials to the BOX due to the implant energy. The influence of a shallow vacancy rich region near the surface could also become more critical as interstitial loss to the BOX occurs. MeV energy Si⁺ implants have been used previously to provide a vacancy rich region closer to the surface. This, in conjunction with boron implantation near the peak of the vacancy profile, allows for I-V recombination to occur and thus reduce BIC formation.⁴⁰ However, the influence of a vacancy rich region produced by low keV implants without the aid of the MeV Si⁺ implantation has not been experimentally observed to affect clustering. Figure 10 shows the fraction of active boron and has been adjusted to account for the dose loss in thin SOI. The fractional active dose was computed by dividing the active dose measured from Hall by the total retained dose within the surface Si layer. It illustrates that the 300 Å SOI is able to approach bulk Si in terms of fractional activation at low temperatures. This further supports that interstitials are being lost to the BOX in thin SOI, thus reducing the BIC population. The dose loss argument explains why the active dose does not approach that of the thick SOI at higher temperatures.

B. Boron segregation

Another theory that could be proposed to explain the lack of activation in SOI is that of boron segregation to the

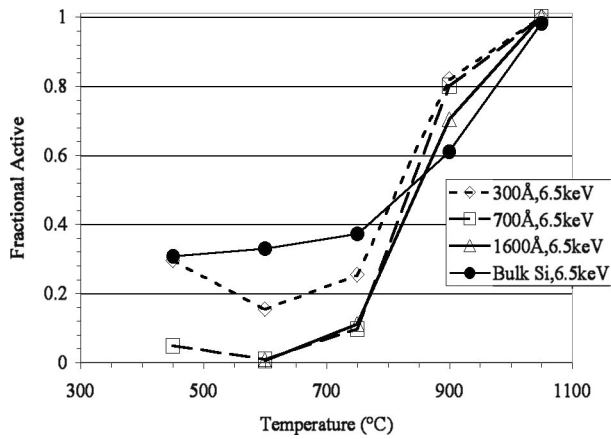


FIG. 10. Active fraction of boron in SOI and bulk for 6.5 keV, $3 \times 10^{14} \text{ cm}^{-2}$ annealed for 30 min.

BOX. Significant segregation could result in dose loss to the BOX that would prevent the boron from activating. It is also not known whether boron pileup at the surface Si/BOX interface is active or not. The theory of dose loss can be disproved by realizing the amount of boron that would be required to segregate to the BOX in thick SOI. Significant segregation/dose loss does not occur in the 1600 Å SOI at the energies studied, so it cannot explain the low activation. Rather, the segregation is limited by the segregation coefficient (~ 0.3 for B) (Ref. 41) for boron in the two materials. Once the chemical potential between the two materials is equilibrated the boron ceases to segregate. The issue of pileup in thin SOI can be addressed by comparing the active dose obtained from Hall to the integrated SIMS dose remaining in the surface Si layer. This results in an active fraction of boron in the 300 Å SOI of 0.2–0.25, while the 1600 Å is closer to 0.1–0.13 (see Fig. 10). This indicates that the segregation in thin SOI does not result in significant deactivation of the boron. Rather, as indicated above, the reduction in BIC population promotes the activation of the boron in thin SOI.

C. Impurity trapping

A third theory might involve contaminants, such as C and O, serving as traps for the interstitials.⁴² If this was the case the C and O could trap the interstitials present in the extended defects.^{43,44} This could explain the QTEM data from our previous work.³⁵ However, it does not explain the QTEM data performed for Si⁺ implants into SOI where no significant difference was observed for implant profiles confined to the surface Si layer.³⁶ This indicates that the presence of boron is the major source of the reduction in trapped interstitials in Ref. 35. Figure 11 shows C and O SIMS profiles obtained for SOI and bulk Si. The carbon levels for SOI and bulk are similar, while slightly more oxygen exists in bulk Si. This difference does not affect the trapping efficiency of extended defects in the bulk, though.

D. Thermal strain

One final theory could be developed based on thermal strain present in SOI. This is due to differences in the linear

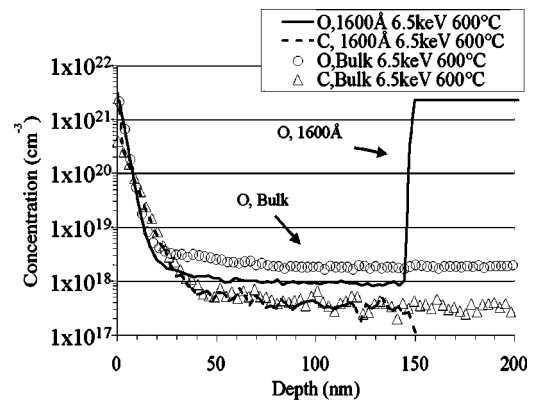


FIG. 11. Carbon and oxygen SIMS profiles for 1600 Å SOI and bulk Si implanted with B⁺, 6.5 keV, $3 \times 10^{14} \text{ cm}^{-2}$, and annealed at 600 °C for 30 min.

thermal expansion coefficient between silicon and silicon dioxide, as well as the BOX and surface Si thickness. These values are $2.6 \times 10^{-6} \text{ }^\circ\text{C}^{-1}$ and $5 \times 10^{-7} \text{ }^\circ\text{C}^{-1}$ for silicon and silicon dioxide, respectively.⁴⁵ Compressive stresses have been shown to significantly affect dopant diffusion in Si depending on their magnitude.^{46–48} Enhancements of $2 \times$ in the diffusivity of boron have been observed for pressures approaching 5 GPa.⁴³ Unfortunately, there have not been many studies to understand the effect of stress on activation in Si. It could be proposed that as SOI is annealed the strain in the surface Si layer increases due to the mismatch, thus preventing the boron from occupying substitutional lattice sites. However, above a certain temperature the BOX begins to viscously flow⁴⁹ and accommodate the strain in the surface Si layer. The process of viscous flow of SiO₂ typically occurs around 1000 °C, but this can be reduced depending on whether or not the SiO₂ is hydrated. This would allow the boron to occupy substitutional sites in the lattice and increase the activation in SOI. This theory could explain the lack of activation in SOI at low temperatures, as well as the activation at higher temperatures.

For tensile thermal stresses, the linear expansion coefficient is related to the change in elongation per unit temperature according to

$$\alpha = \frac{\Delta l}{l_0(T - T_0)}, \quad (1)$$

where α is the linear thermal expansion coefficient, Δl is the change in elongation, l_0 is the original length, and ΔT is the change in temperature in Kelvin. The tensile strain is given as

$$\varepsilon = \frac{\Delta l}{l_0} = \alpha(T - T_0), \quad (2)$$

where ε is the engineering strain. Using Hooke's Law, the thermal stress is

$$\sigma = E\varepsilon = E\alpha(T - T_0), \quad (3)$$

where σ is stress. Stoney⁵⁰ developed a method, based on the mechanics of beam bending, by which the stress in thin films on substrates may be calculated. Stoney's formula is commonly stated as

$$\alpha_f = \frac{F_f}{d_f w} = \frac{1}{6R(1-\nu_s)} \frac{E_s d_s^2}{d_f}, \quad (4)$$

where σ_f is the film stress, F_f the force exerted by the thin film, d_f the film thickness, w the width of the film, R the radius of curvature of the film, E_s the elastic modulus of the substrate material, d_s the substrate thickness, and ν_s Poisson's ratio of the substrate.⁵¹ Combining the effects of thermal strain and mechanical strain the total strain in the film and substrate are given as

$$\varepsilon_f = \alpha_f \Delta T + \frac{F_f(1-\nu_f)}{E_f d_f w}, \quad (5)$$

$$\varepsilon_s = \alpha_s \Delta T - \frac{F_f(1-\nu_s)}{E_s d_s w}. \quad (6)$$

In order for mechanical equilibrium to hold, ε_f must be equal to ε_s . This allows for calculation of the thermal mismatch force F_f by manipulating Eqs. (5) and (6). Since $d_s \gg d_f$, for case involving partially depleted SOI and fully depleted SOI, the film stress due to thermal mismatch between a film and its substrate can be stated as

$$\alpha_f(T) = \frac{F_f}{d_f w} = \frac{(\alpha_s - \alpha_f) \Delta T E_f}{(1 - \nu_f)}. \quad (7)$$

SOI can be thought of as a multilayer structure consisting of two thin films on a bulk Si substrate. During annealing, the Si substrate expands at a greater rate than the BOX, thus creating a tensile stress in the overlying BOX. As SOI is allowed to cool down, the substrate also contracts at a greater rate than the BOX, allowing for a residual compressive stress to form in the layers. The intrinsic compressive stress in thermally grown SiO₂ has been measured to be between -0.2 GPa and -0.3 GPa, which is close to that predicted by Eq. (7) assuming viscous flow above 1000 °C.⁵² According to Hooke's Law, this would correspond to a strain of 0.25%–0.38% in the BOX. However, this stress would tend to be reduced during annealing since the films would expand/contract oppositely from that occurring during cool down. This pressure would not be significantly high enough to enhance the diffusivity of B in Si according to Zhao *et al.*,⁴⁶ thus tending to shed doubt on this theory. SIMS profiles on the thick SOI and bulk Si confirmed that this was the case, as the diffusivity within the tail of the profiles appeared similar.

V. CONCLUSIONS

We have investigated the process of electrical activation of boron in SOI scaled to 300 Å using Hall effect, four-point probe, and SIMS. We show a significant decrease in the active dose of boron in SOI compared to bulk Si at low annealing temperatures. Lower mobility and increased sheet resistance are also observed, but the mobility in SOI is close to that observed previously. Above approximately 900 °C the boron active dose in SOI approaches that of bulk Si. All these effects appear regardless of the surface Si thickness and implant energy. It is also shown that as the implant energy increases, fractional activation in thin SOI increases, likely due to a reduction in boron interstitial clusters in the

surface Si layer. A number of theories are proposed to explain the lack of activation in SOI, but boron interstitial clustering appears the most likely source. These results could have significant impact on low temperature processes in SOI such as solid phase epitaxy, as well as high temperature annealing using high ramp rates. Future work will involve an in depth high-resolution x-ray diffraction study to determine the precise role of strain in thermal processing of SOI.

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